



Appl. No. 09/640,729

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Axel Hertwig, et al.  
Appl. No. : 09/640,729  
Filed : 17 August 2000  
Title : Multiprocessor System

Grp./Art Unit : 2181  
Examiner: R. Phan

Docket No. : PHD 99107

Assistant Commissioner for Patents

**RESPONSE and AMENDMENT**

Sir:

This is in response to the Office Action of 18 March 2003.

**RECEIVED**

**MAY 20 2003**

**Technology Center 2100**

IN THE TITLE

Please delete the present title and insert therefore the following new title:

**MEMORY SHARING ARRANGEMENT FOR AN INTEGRATED  
MULTIPROCESSOR SYSTEM**

IN THE CLAIMS

Please cancel Claims 1-16 without prejudice or disclaimer.

Please add new Claims 17-28 as follows.

- 1 **17.** (New) An electronic product, comprising:
- 2 a first processor coupled to an instruction cache and to a data cache;
- 3 a first bus coupled to the instruction cache and to the data cache;
- 4 a first memory coupled to the first bus;
- 5 a second processor coupled to a second bus;